Kingbright

L-1503EB/111YD

T-1 3/4 (5mm) Bi-Level Circuit Board Indicator

DESCRIPTIONS

- The High Efficiency Red source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Orange Light Emitting Diode
- The Yellow source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Yellow Light Emitting Diode

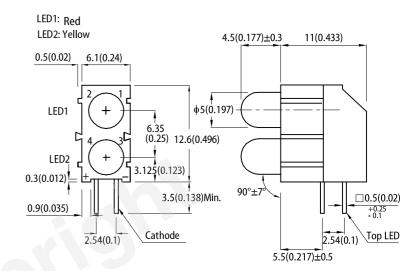
FEATURES

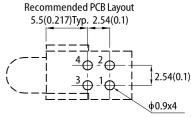
- · Pre-trimmed leads for pc board mounting
- · Stackable units
- · Colors can be mixed in a single housing
- · Black case enhances contrast ratio
- · High reliability-life measured in years
- Housing UL rating: 94V-0
- · Housing material: Type 66 nylon
- RoHS compliant

APPLICATIONS

- · Status indicator
- Illuminator
- · Signage applications
- Decorative and entertainment lighting
- · Commercial and residential architectural lighting

PACKAGE DIMENSIONS





Notes

- Notes:

 1. All dimensions are in millimeters (inches).

 2. Tolerance is ±0.25(0.01") unless otherwise noted.

 3. Lead spacing is measured where the leads emerge from the package.

 4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

SELECTION GUIDE

Part Number	Emitting Color (Material)	Lens Type	Iv (mcd) @ 10mA [2]		Viewing Angle [1]
			Min.	Тур.	201/2
L-1503EB/1I1YD	■ High Efficiency Red (GaAsP/GaP)	Red Diffused	25	50	30°
			*12	*40	
	Yellow (GaAsP/GaP)	Yellow Diffused	15	30	30°
			*15	*30	

1. 61/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
2. Luminous intensity / luminous flux: +/-15%.

* Luminous intensity value is traceable to CIE127-2007 standards.





ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

Downworks:	Symbol	Facilities Octor	Value		1114
Parameter		Emitting Color	Тур.	Max.	Unit
Wavelength at Peak Emission I _F = 10mA	λ_{peak}	High Efficiency Red Yellow	627 590	-	nm
Dominant Wavelength I _F = 10mA	λ _{dom} ^[1]	High Efficiency Red Yellow	617 588	-	nm
Spectral Bandwidth at 50% Φ REL MAX I _F = 10mA	Δλ	High Efficiency Red Yellow	45 35	-	nm
Capacitance	С	High Efficiency Red Yellow	15 20	-	pF
Forward Voltage I _F = 10mA	V _F ^[2]	High Efficiency Red Yellow	1.9 1.95	2.3 2.4	V
Reverse Current (V _R = 5V)	I _R	High Efficiency Red Yellow	-	10 10	μА
Temperature Coefficient of λ_{peak} I_F = 10mA, -10°C $\leq T \leq 85^{\circ}C$	TC _{λpeak}	High Efficiency Red Yellow	0.13 0.12	-	nm/°C
Temperature Coefficient of λ_{dom} I_F = 10mA, -10°C $\leq T \leq 85^{\circ}C$	TC _{λdom}	High Efficiency Red Yellow	0.06 0.07	-	nm/°C
Temperature Coefficient of V_F I_F = 10mA, -10°C \leq T \leq 85°C	TCv	High Efficiency Red Yellow	-1.9 -2	-	mV/°C

Notes:

ABSOLUTE MAXIMUM RATINGS at T_A=25°C

Parameter	Symbol	Val	Unit		
Farameter		High Efficiency Red	Yellow	Offic	
Power Dissipation	P_D	75	75	mW	
Reverse Voltage	V _R	5		V	
Junction Temperature	T _j	125	110	°C	
Operating Temperature	T _{op}	-40 to +85		°C	
Storage Temperature	T _{stg}	-40 to +85		°C	
DC Forward Current	I _F	30	30	mA	
Peak Forward Current	I _{FM} ^[1]	160	140	mA	
Electrostatic Discharge Threshold (HBM)	-	8000	8000	V	
Thermal Resistance (Junction / Ambient)	R _{th JA} ^[2]	490	600	°C/W	
Thermal Resistance (Junction / Solder point)	R _{th JS} ^[2]	230	420	°C/W	
Lead Solder Temperature [3]		260°C For 3 Seconds			
Lead Solder Temperature [4]		260°C For 5 Seconds			

Notes:

1. The dominant wavelength (\(\lambda\d)\) above is the setup value of the sorting machine. (Tolerance \(\lambda\d: \pm 1 nm.\))

2. Forward voltage: \(\pm 2.1V.\)

3. Wavelength value is traceable to CIE127-2007 standards.

4. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

Notes:

1. 1/10 Duty Cycle, 0.1ms Pulse Width.

2. R_{In. M.} R_{In. JS} Results from mounting on PC board FR4 (pad size ≥ 16 mm² per pad).

3. 2mm below package base.

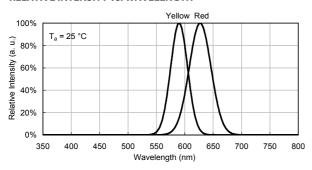
4. 5mm below package base.

5. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

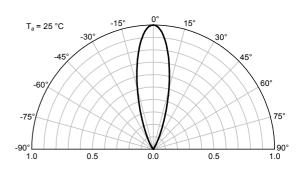
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TECHNICAL DATA

RELATIVE INTENSITY vs. WAVELENGTH

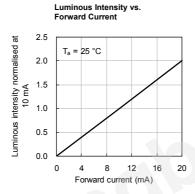


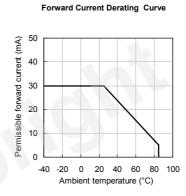
SPATIAL DISTRIBUTION

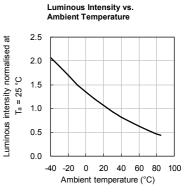


HIGH EFFICIENCY RED

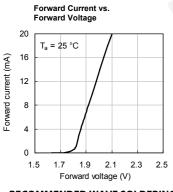
Forward Current vs. Forward Voltage 20 $T_a = 25$ °C 16 Forward current (mA) 12 8 1.5 1.7 1.9 2.1 1.3 Forward voltage (V)

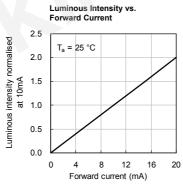


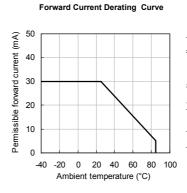


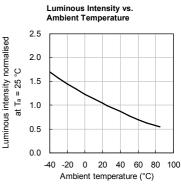


YELLOW

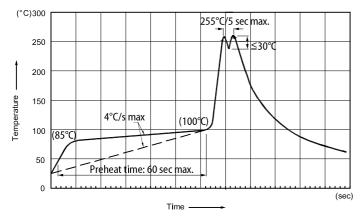








RECOMMENDED WAVE SOLDERING PROFILE



- 1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C

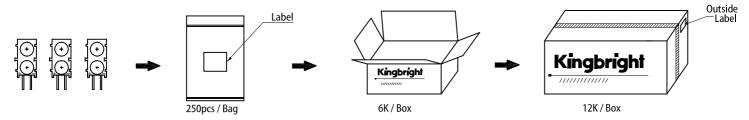
- 2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).

 3. Do not apply stress to the epoxy resin while the temperature is above 85°C.

 4. Fixtures should not incur stress on the component when mounting and during soldering process.
- 5. SAC 305 solder alloy is recommended.6. No more than one wave soldering pass.

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PACKING & LABEL SPECIFICATIONS





PRECAUTIONS

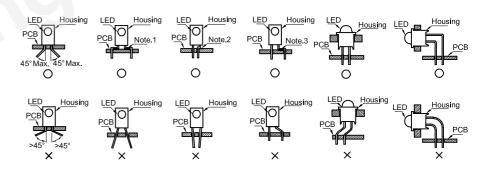
Storage Conditions

- 1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- 2. LEDs should be stored with temperature ≤ 30°C and relative humidity < 60%.
- 3. Product in the original sealed package is recommended to be assembled within 72 hours of opening. Product in opened package for more than a week should be baked for 30 (+10/-0) hours at 85 ~ 100°C.

LED Mounting Method

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures.

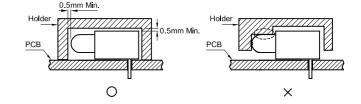
Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.



Correct mounting method " x " Incorrect mounting method

Lead Forming Procedures

- 1. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering.
- 2. The tip of the soldering iron should never touch the lens epoxy.
- 3. Through-hole LEDs are incompatible with reflow soldering.
- 4. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.



PRECAUTIONARY NOTES

- The information included in this document reflects representative usage scenarios and is intended for technical reference only
- The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications
- When using the products referenced in this document, please make sure the product is being operated within the environmental and electrical limits specified in the datasheet. If customer usage exceeds the specified limits, Kingbright will not be responsible for any subsequent issues.

 The information in this document applies to typical usage in consumer electronics applications. If customer's application has special reliability requirements or have life-threatening
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